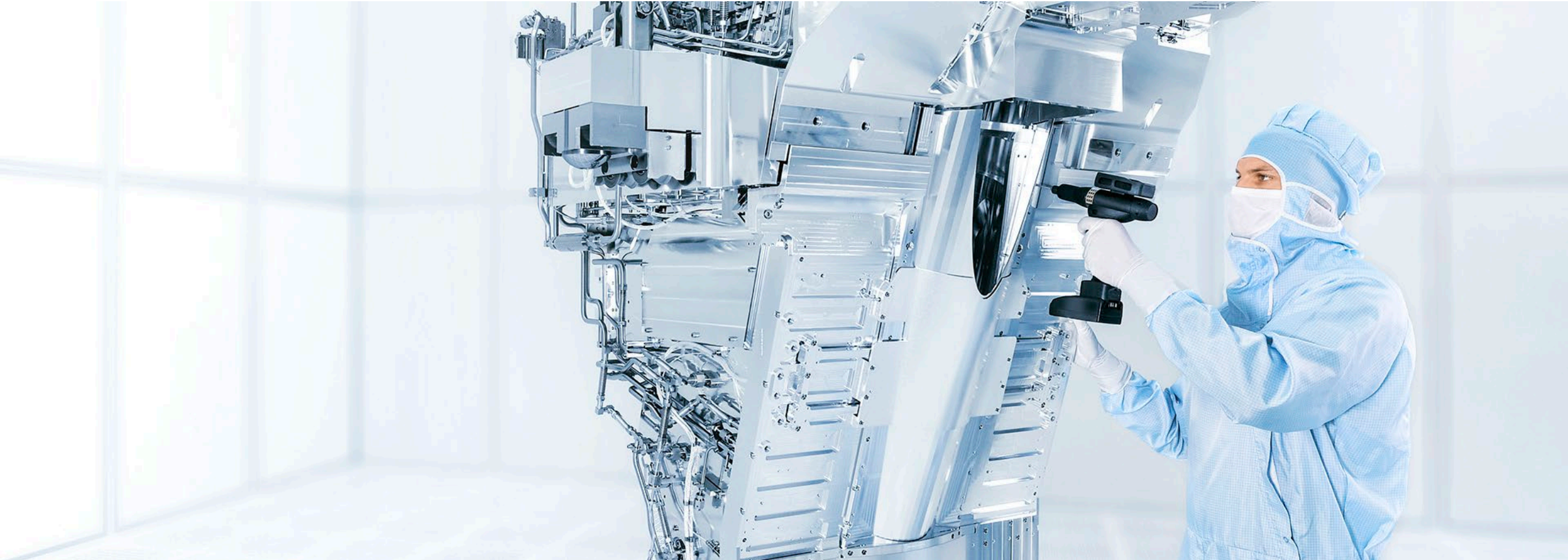


High NA EUV optics: a big step in lithographic resolution



Lars Wischmeier¹, Paul Graeupner¹, Peter Kuerz¹, Judon Stoeldraijer², Jan van Schoot²

¹ZEISS, ²ASML

EUVL Workshop 2021


Customer flagship products are powered with 7nm+ EUV



7nm EUV

Performance and efficiency reimagined

Power efficiency and performance come first with the Exynos 9825, the industry's first mobile processor built with 7nm EUV processing technology. EUV, or extreme ultraviolet lithography, allows Samsung to leverage extreme ultraviolet wavelengths to print finer circuits and develop a faster and more power efficient processor.



HUAWEI Kirin 990 Series¹


Rethink Evolution

World's 1st Flagship 5G SoC powered with 7nm+ EUV²



More than Renovation

As the world's 1st Flagship 5G SoC powered with 7nm+ EUV³, the Kirin 990 5G features breakthrough technology and advanced intelligence, inherited from Kirin and Balong. Thanks to the 7nm+ EUV technology, over 10 billion transistors⁴ are condensed in this tiny chipset. The Kirin 990 5G ushers in the future with superior performance.



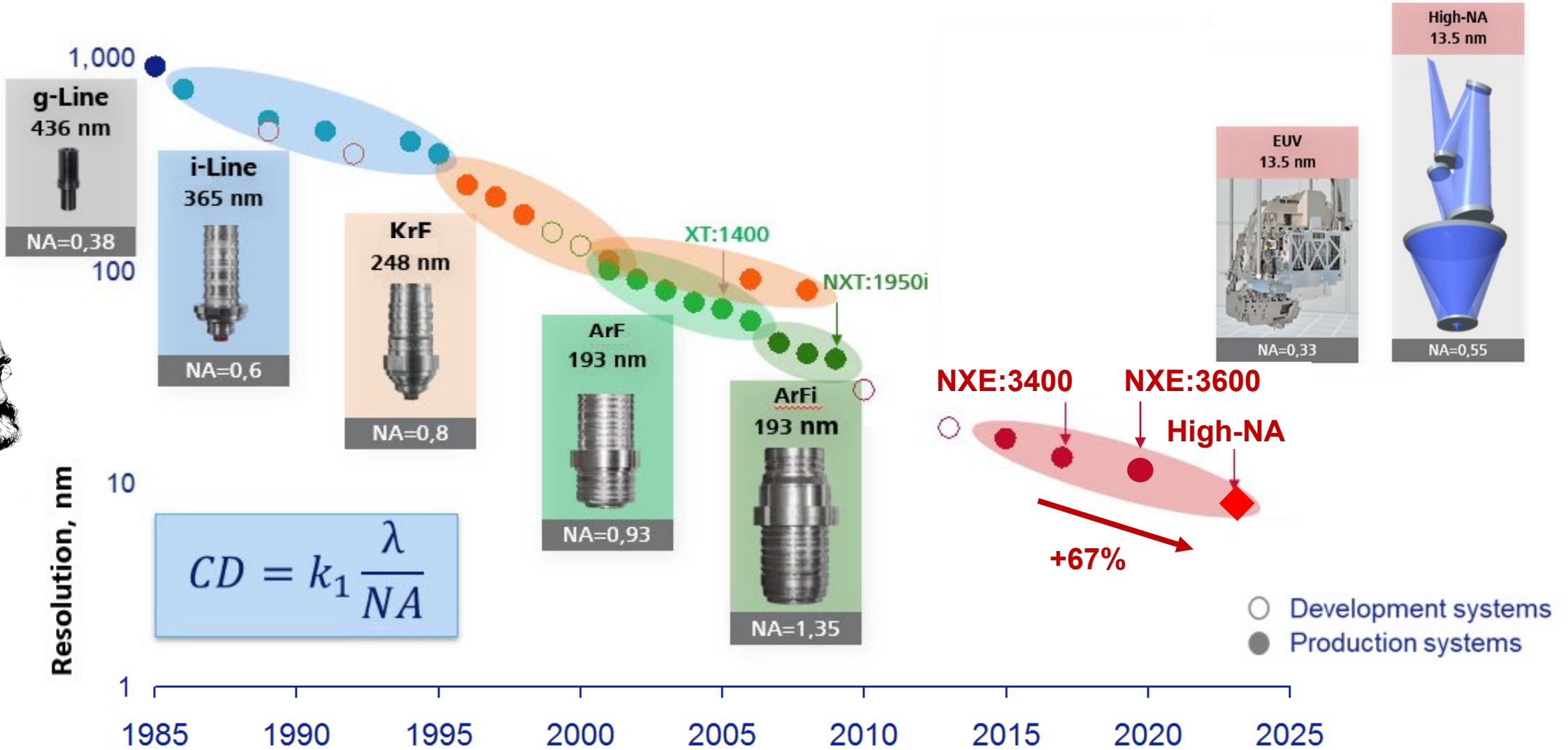
- Machine learning controller
- New 6-core CPU
- Next-generation ML accelerators
- 16-core NEURAL ENGINE
- 5 nanometer process
- 11.8 billion Transistors
- 11 trillion Operations per second
- Advanced image signal processor
- New 4-core GPU
- Secure Enclave



- 5 nanometer process
- Machine learning accelerators
- 16-core Neural Engine
- 11 trillion operations per second
- Thunderbolt / USB 4 controller
- Media encode and decode engines
- 16 billion transistors
- Up to 8-core GPU
- 8-core CPU
- Advanced image signal processor
- Secure Enclave
- Unified memory architecture
- Industry-leading performance per watt

Source: <https://www.samsung.com/semiconductor/minisite/exynos/products/mobileprocessor/exynos-9825/> , <https://consumer.huawei.com/en/campaign/kirin-990-series/>

Next logical step on lithography roadmap is a High-NA EUV system



- 1 New 0.33NA EUV optics
- 2 Design features of High-NA EUV optics
- 3 Manufacturing of High-NA EUV mirrors and frames

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Starlith 3600[®]: Extending the Roadmap for 0.33NA

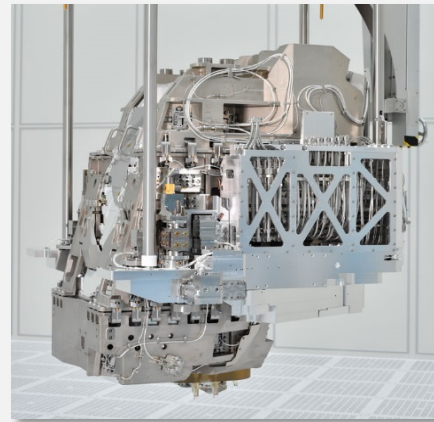
EUV aberration roadmap continues at higher source power



3600 Illuminator

Key features:

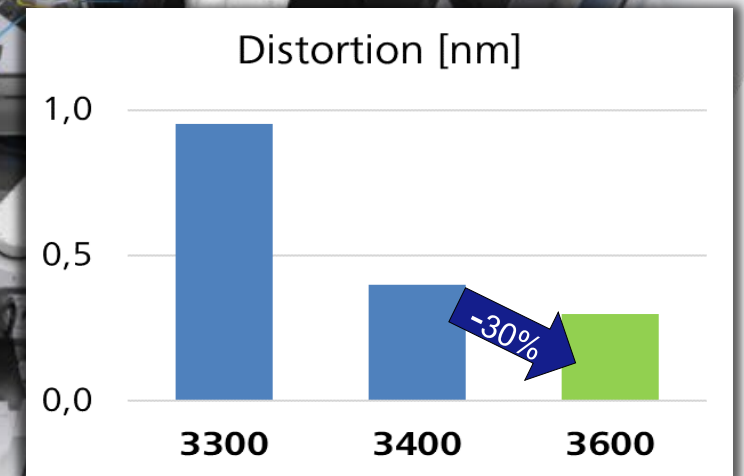
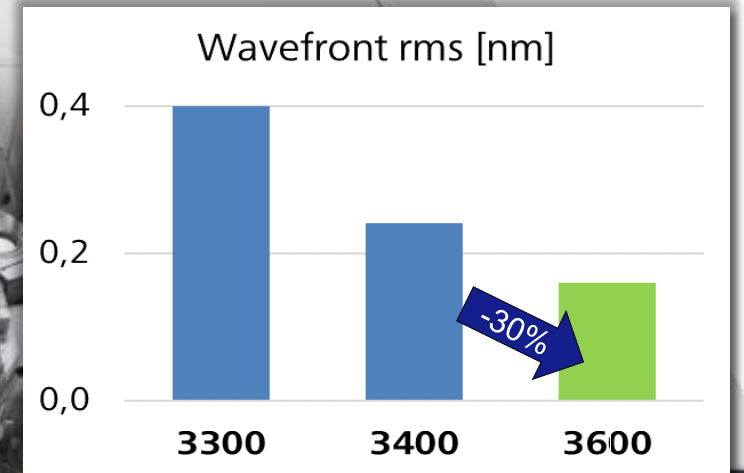
- Support of higher EUV power
- Improved transmission



3600 Projection Optics

Key features:

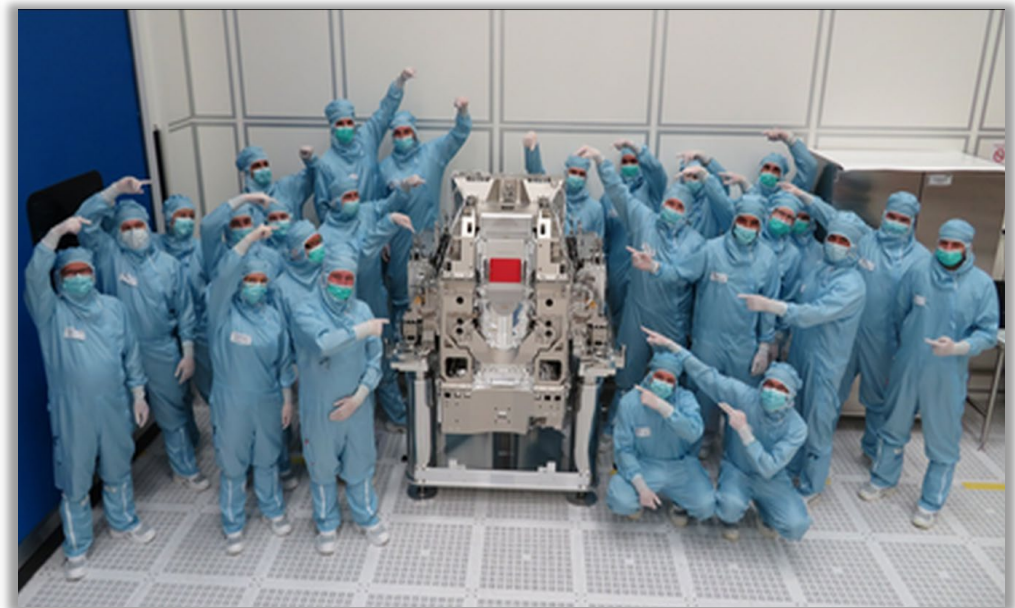
- Support of higher EUV power
- Improved Wavefront Performance
- Improved Disto



First Starlith 3600[®] Illuminator delivery



First Starlith 3600[®] POB delivery

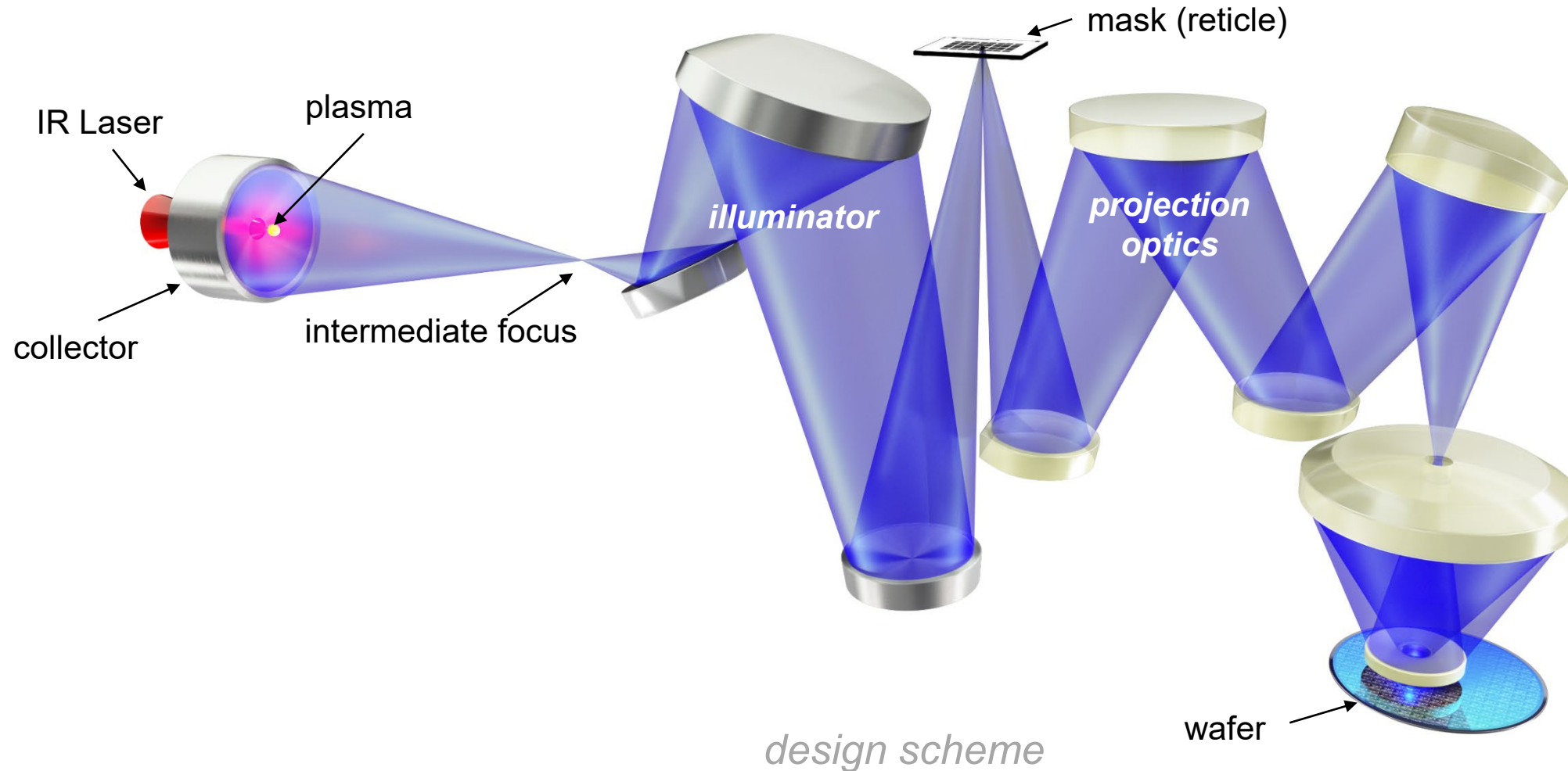


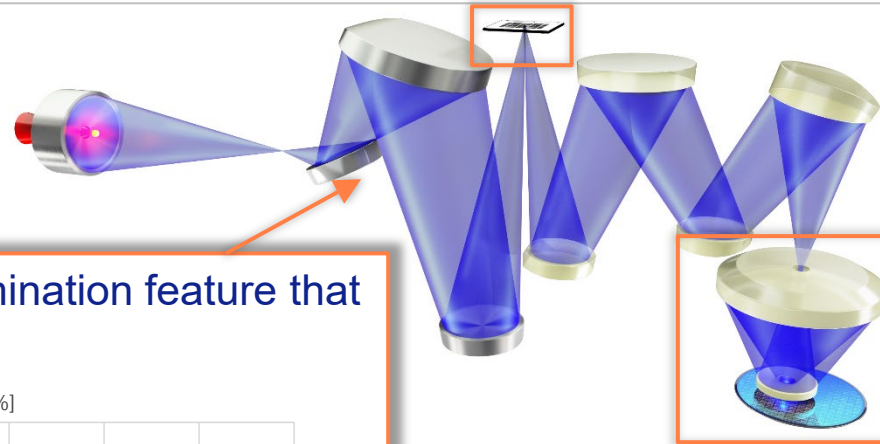
- Delivery of more than 140 EUV systems with 0.33 NA at high and robust performance.
- More to come due to strong market pull.

- 1 New 0.33NA EUV optics
- 2 Design features of High-NA EUV optics
- 3 Manufacturing of High-NA EUV mirrors and frames

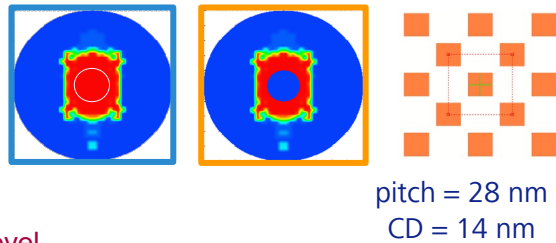
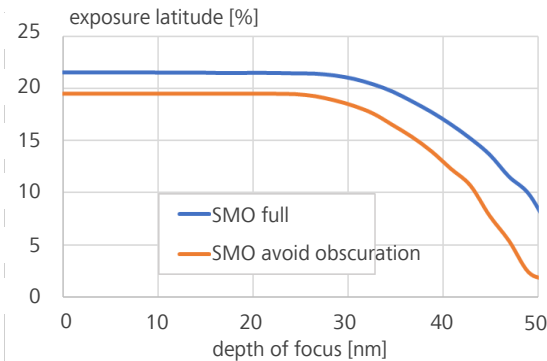
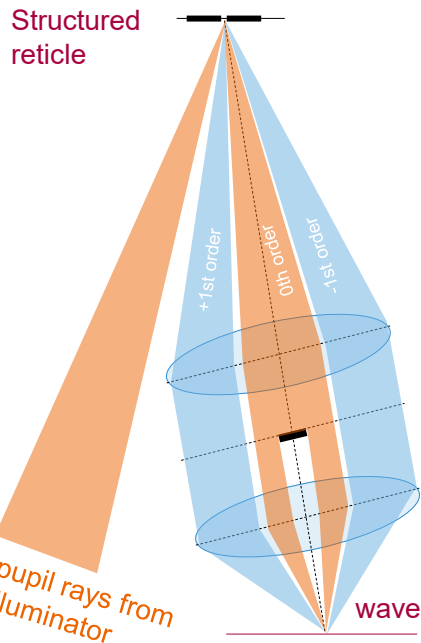
High-NA EUV

The optical system for the ultimate printing machine with $NA = 0.55$

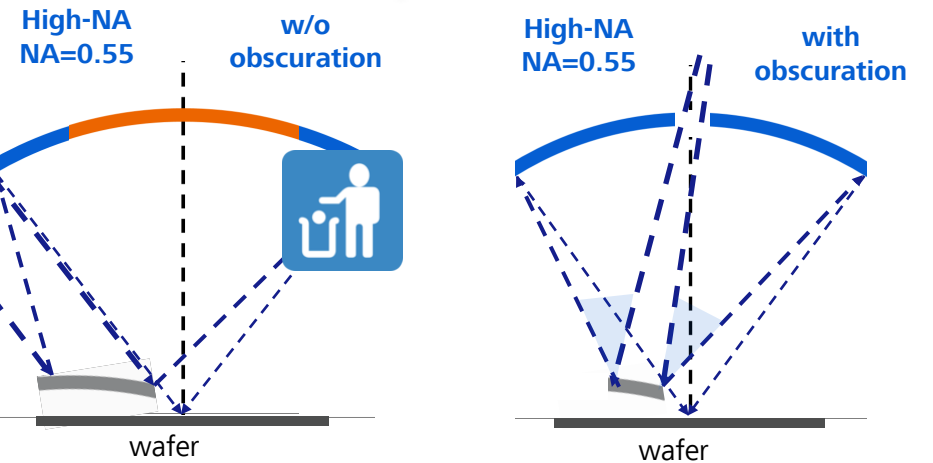




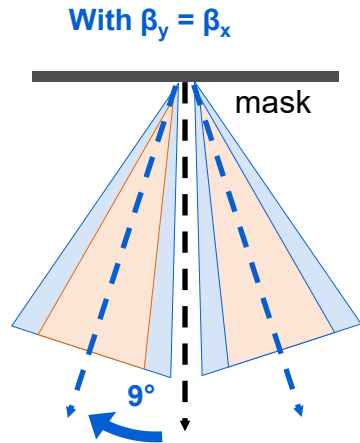
Obscuration can be used for new illumination feature that can improve process window



¹ van Setten et al, Proc. SPIE 1095709

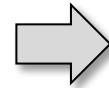


- Obscuration reduces angular load and angle variation
- High-transmission design solution.

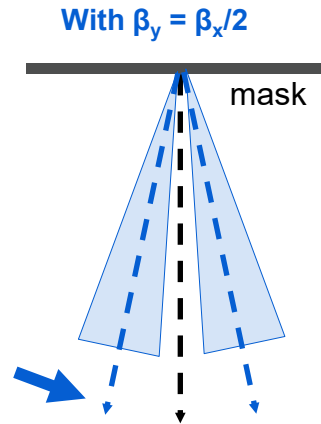


solution

anamorphic design



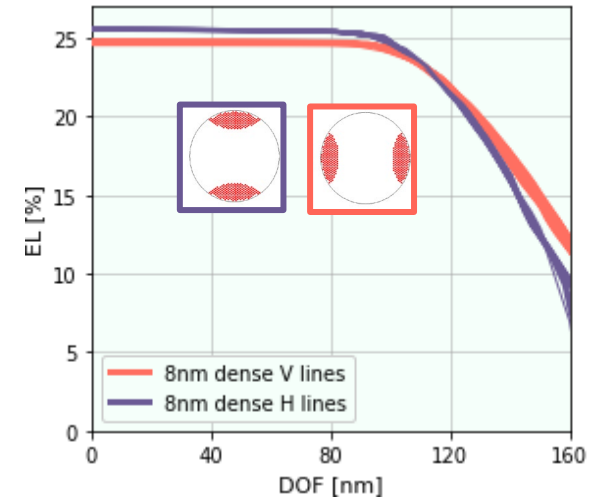
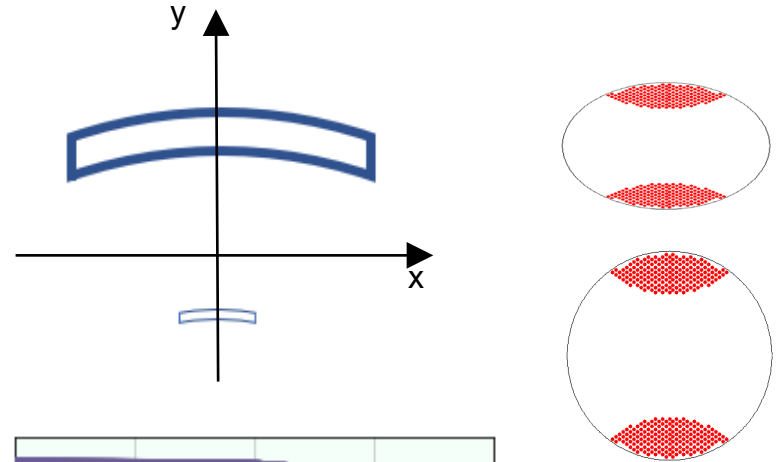
NA @ mask
= $\beta \times$ NA @ wafer
 $\beta_{scan} = 1/8$
 $\beta_{x-scan} = 1/4$



- absorber shadowing at mask is angular dependent
- Inacceptable contrast loss for system with $\beta_{scan} = 1/4$

Reticle Level
104x12.8 mm²

Wafer Level
26x1.6 mm²

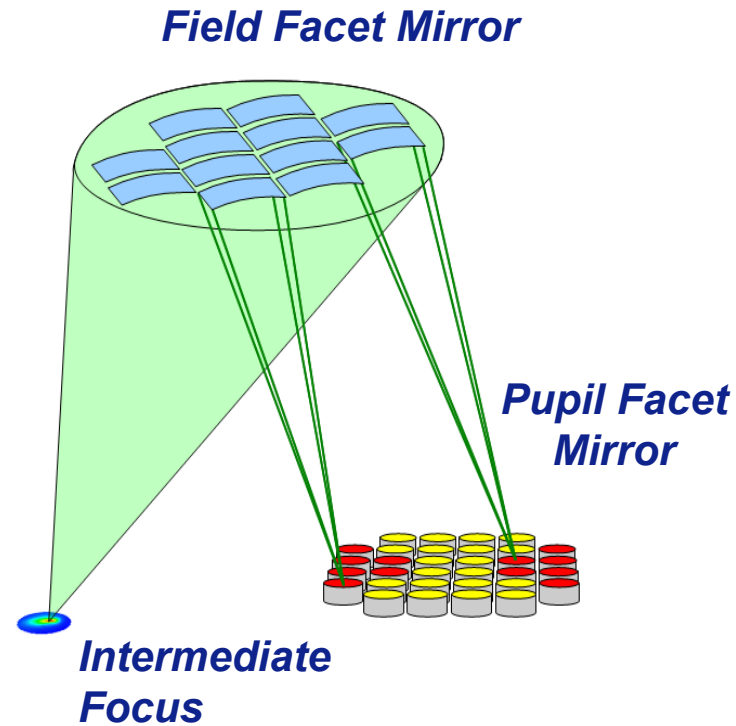
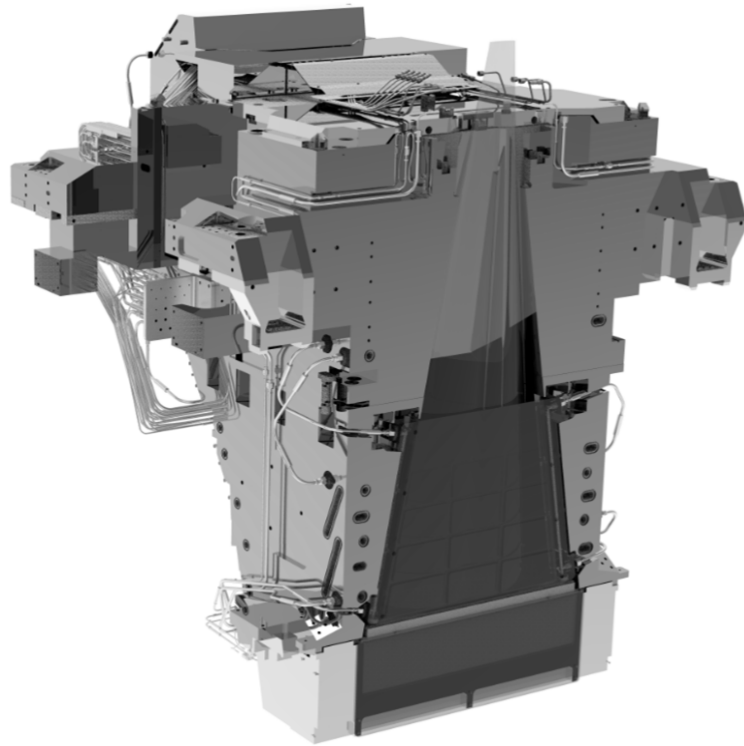


**Resolving power at wafer is isotropic
resolution_x = resolution_y**

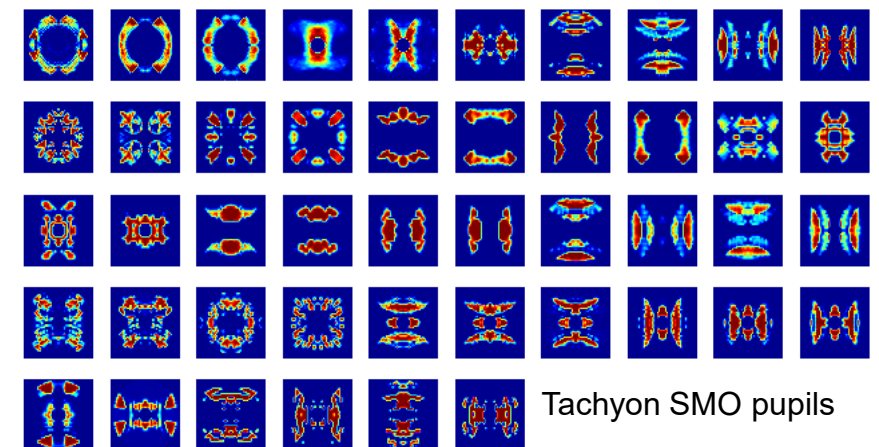
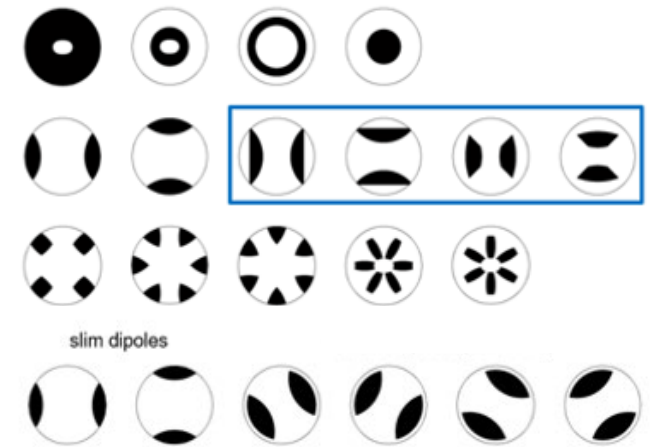
Bartosz Bilski, Photomask Japan 2021

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High-NA illuminator will utilize 0.33NA technology with actuated facet mirrors



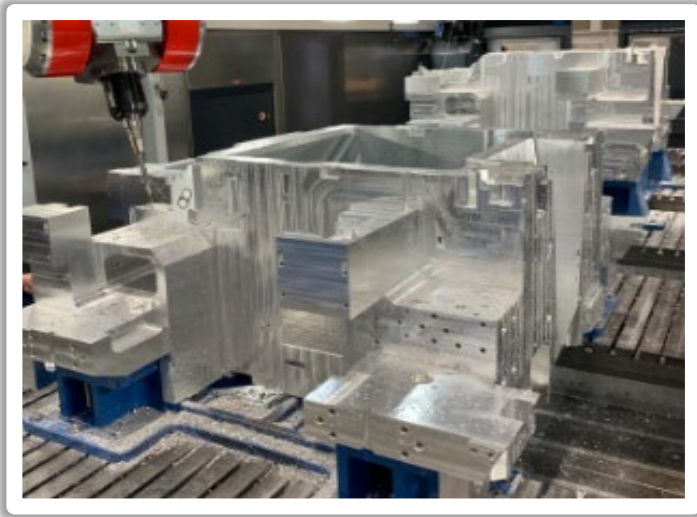
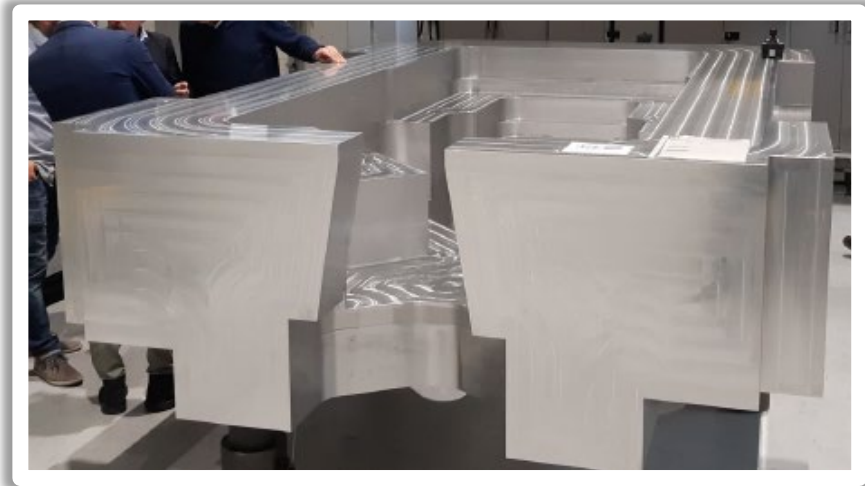
Standard Illumination Settings



- High-NA illuminator provides 20% pupil fill ratio
- More focus on medium sigma dipoles (→ mask 3D effects)

First frames for illuminator and POB processed

Further sets already in production



Build up of system integration tooling is progressing

>20.000 mechatronic parts & components

assembly & qualification on module level

mirror glueing



mirror module assembly



dynamic qualification



assembly & qualification on sensor frame level

sensor frame & interferometer assembly



alignment & qualification



assembly & cleaning

force frame & module integration



cleaning & leak check



transports

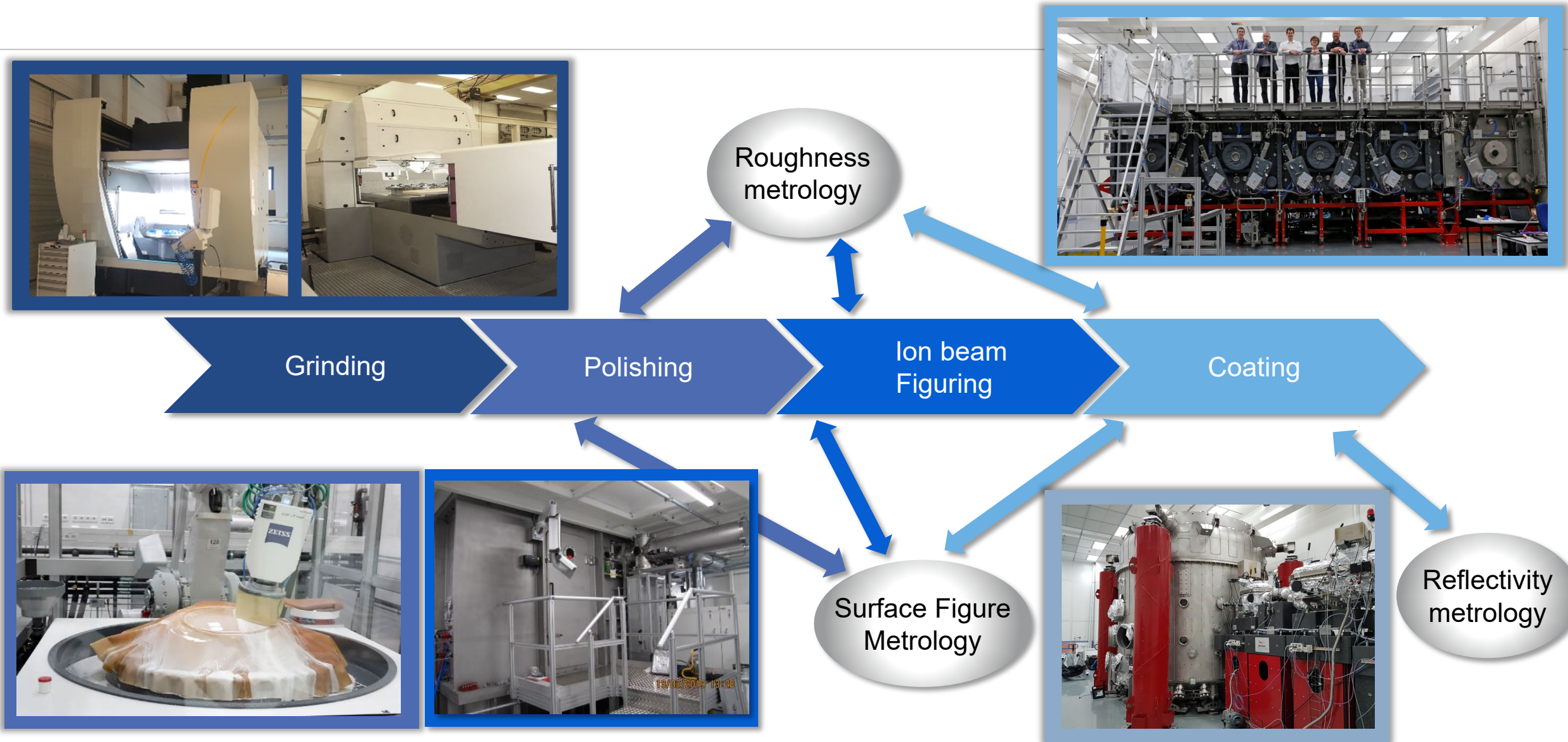
module transport



frame transport



Optics manufacturing process

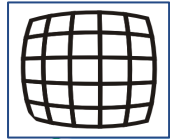


First mirror ground

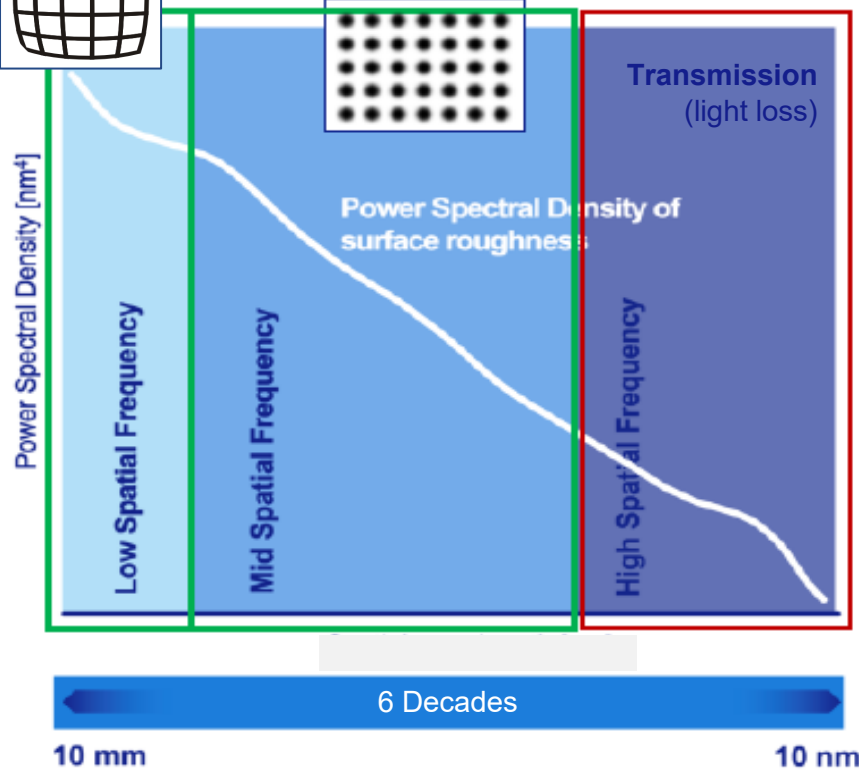
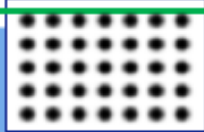


Critical quality parameters for polishing of optical surfaces

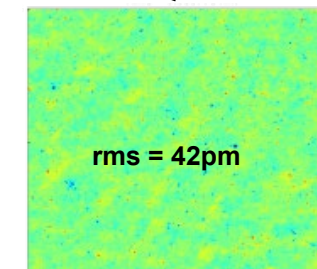
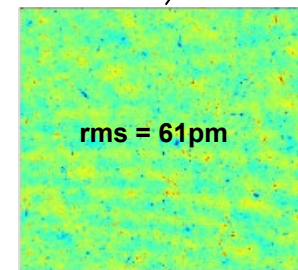
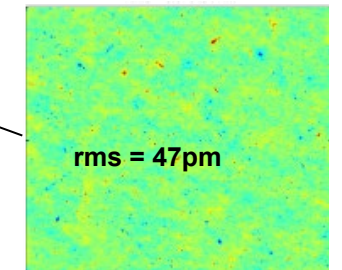
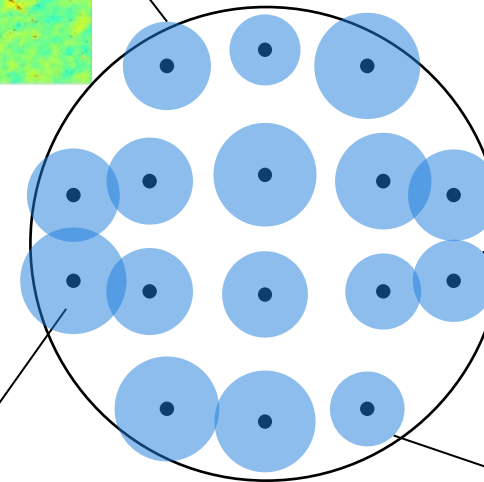
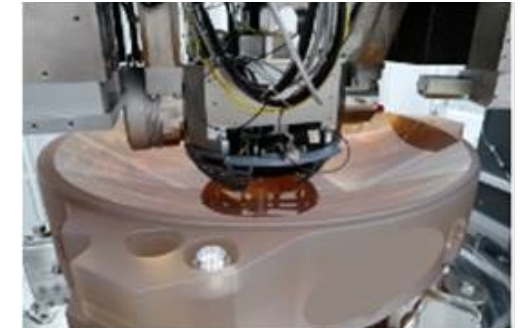
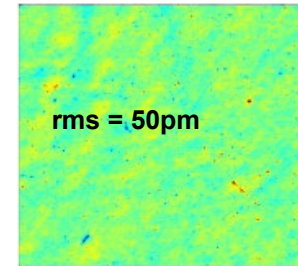
CD Uniformity, Overlay
(aberrations)



Uniformity, Contrast
(Scattering light in the illuminated field)

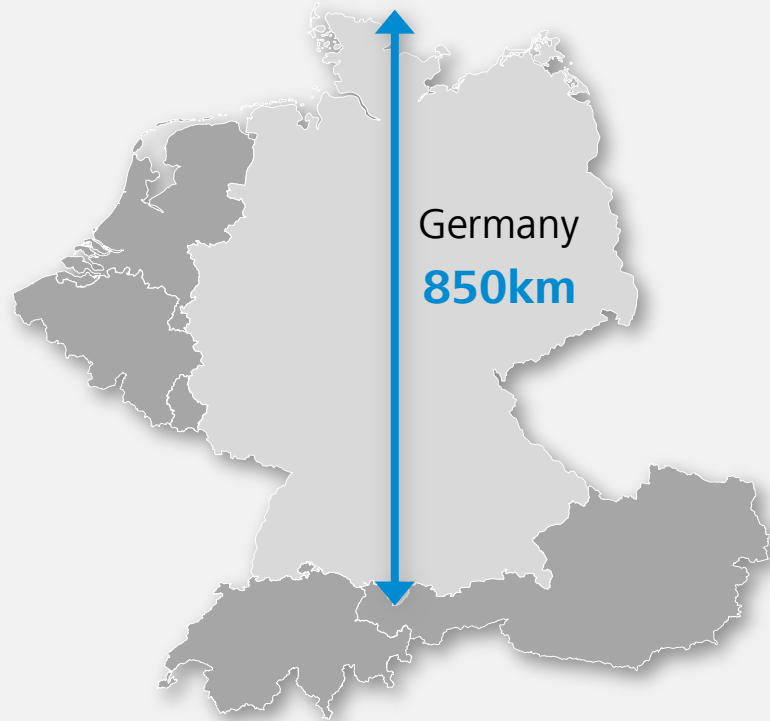


Micro Interferometer (Field of View ~5mm) for mid spatial frequency range



Challenges scale with accuracy and size

Atomic level figuring required



Zugspitze
2962m



High-NA mirror

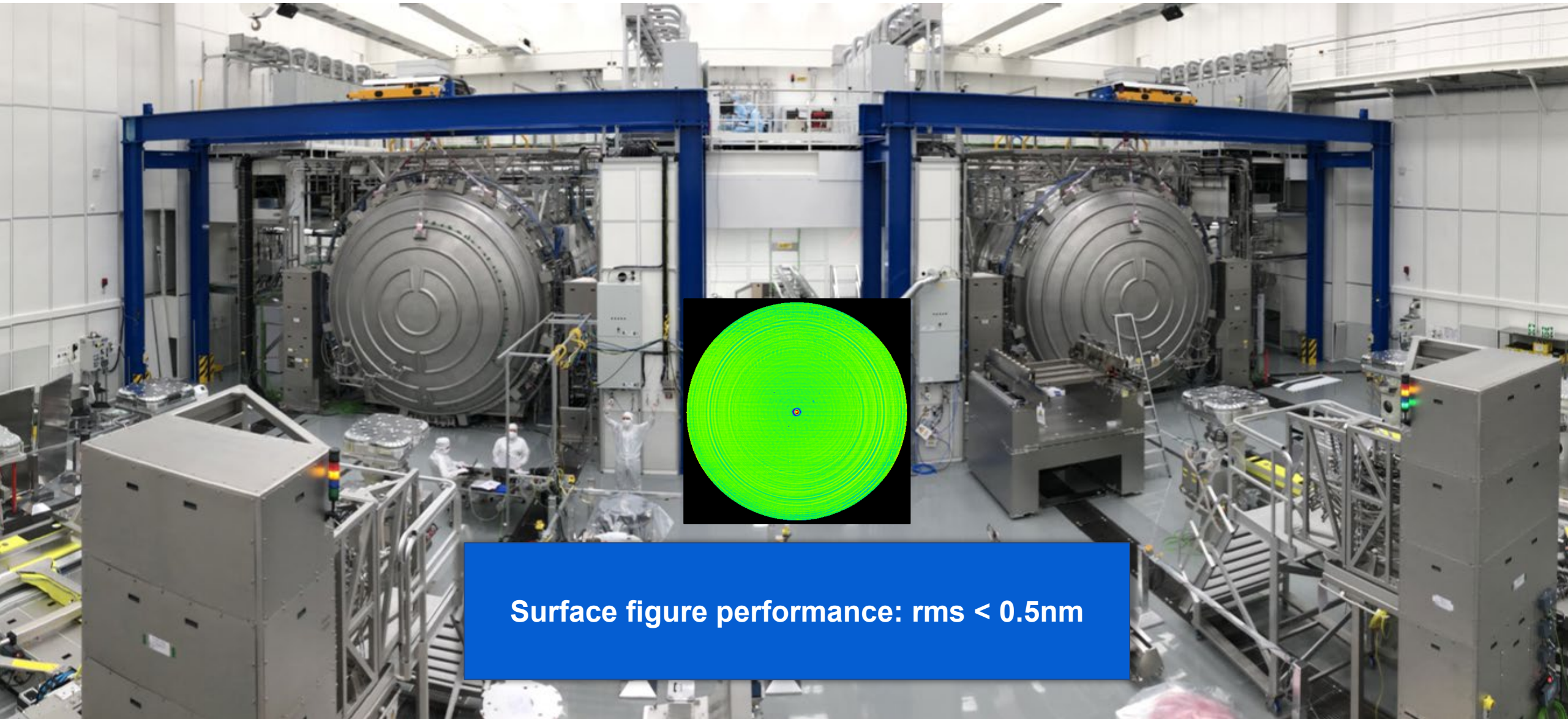


RMS ~ 20 pm

Scale to size of Germany
by ~1 million

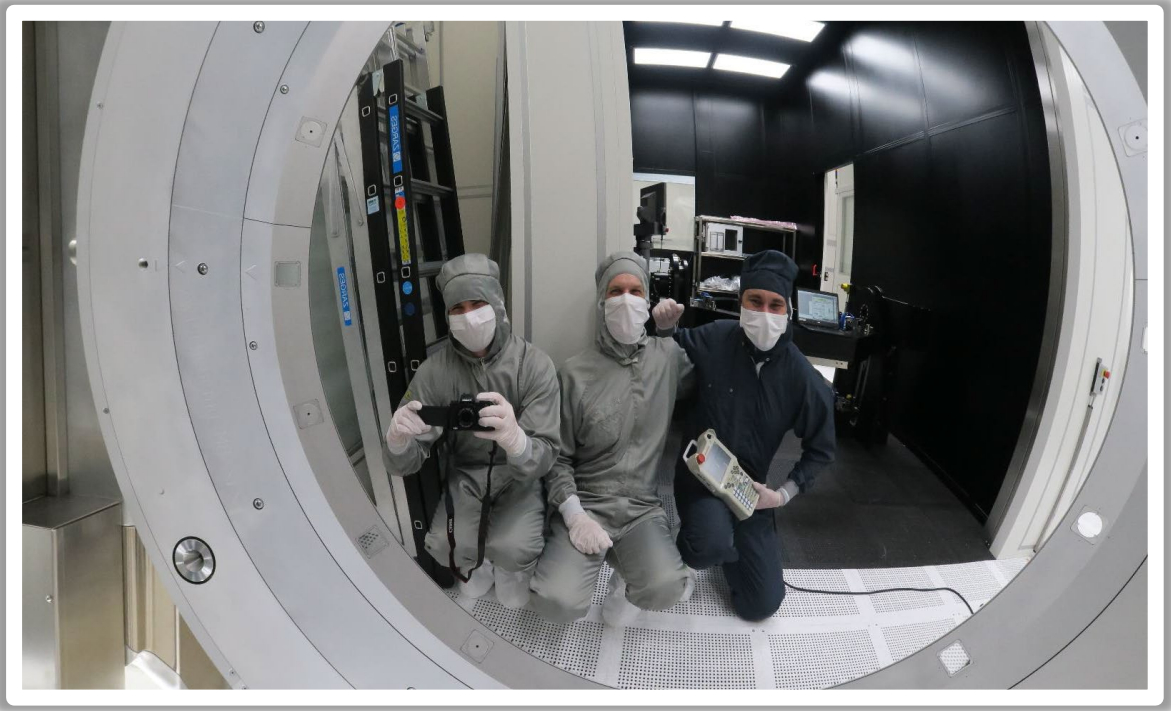
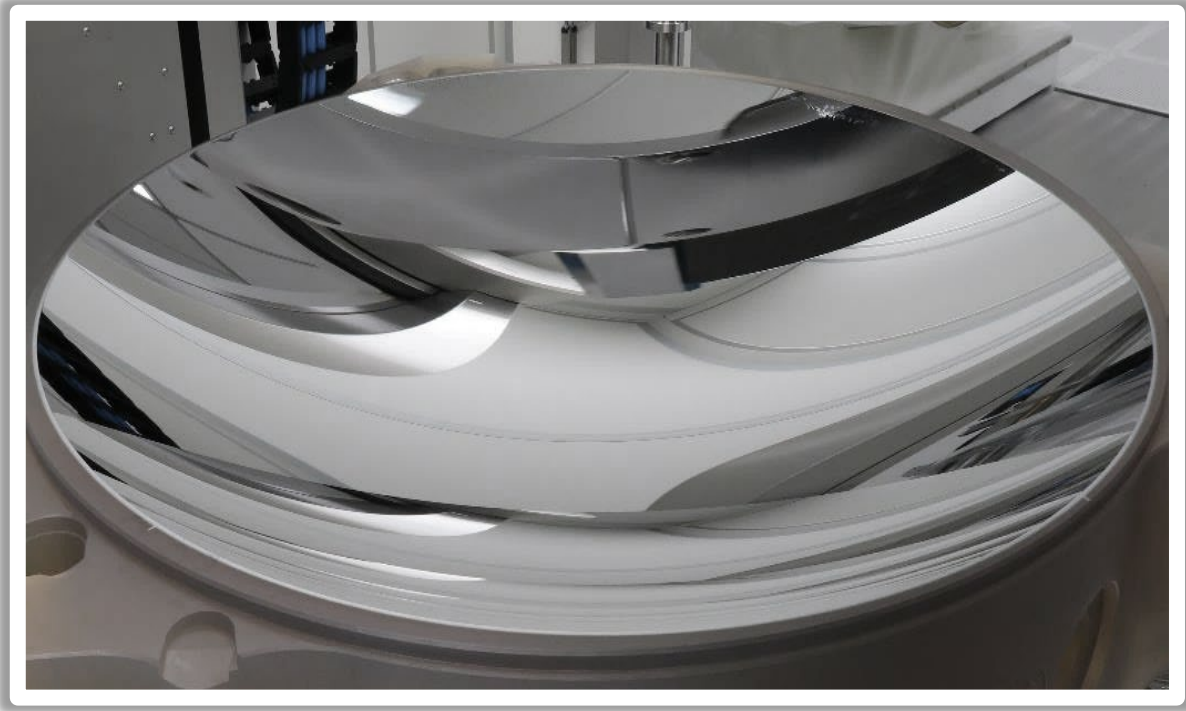
*heights of ~20 μ m in Germany
or
1/5 of a human hair*

Mirror metrology is operational and supports mirror manufacturing requirements

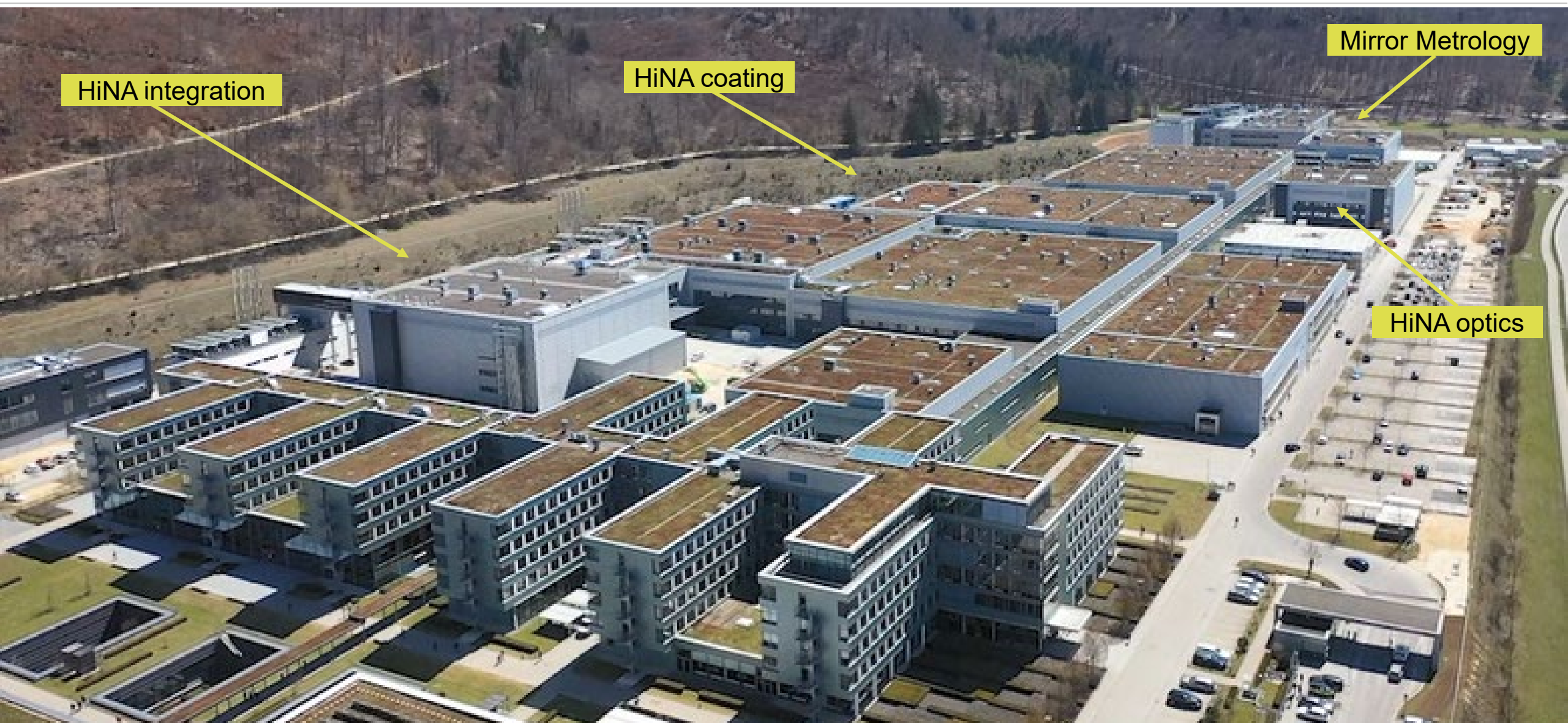


Surface figure performance: rms < 0.5nm

First dummy coated



ZEISS SMT Campus Oberkochen



HiNA integration

HiNA coating

Mirror Metrology

HiNA optics

Summary



- We are producing mirrors and frames for High-NA EUV optics at full speed.
- Mirror performance approaches specification level in sub-nanometer regime
- Build up of system integration tools is progressing.



Special thanks goes to:

Many, many people, too numerous to mention all by name

The high-NA teams at ASML and ZEISS

Our suppliers, customers, and project partners around the globe

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Thank you!

